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Modeling the reduction of thermal conductivity in core/shell and diameter-modulated silicon nanowires

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The thermal conductivity of a series of structural, diameter, and core/shell modulated silicon nanowires is computed by molecular dynamics simulations using the nonequilibrium method. The purpose of this study is to appraise the impact of surface roughness, amorphous parts integration, and diameter modulation on the thermal conductivity reduction. The addition of both amorphous regions and nanoconstrictions allows us to reach a thermal conductivity close to that of the amorphous pristine nanowires, while preserving a certain degree of crystallinity. This could result in interesting properties for thermoelectric applications.

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I. INTRODUCTION

Finding innovative thermoelectric materials is the object of intense research today. There are several challenges to address like the efficiency, the sustainability, and the processibility of these materials. However, silicon based nanostructures offer alternative routes, with the advantages of good engineering and low production cost, which will both be important in the near future for large scale applications for thermoelectric technologies. In this respect, to balance the weak efficiency of bulk material properties of silicon compared to other thermoelectric materials, innovative concepts and devices are important. Due to the well-controlled production processes, those are possible for silicon, but unfortunately not for other thermoelectric materials. Such possibilities are investigated in the present work through the use of nanostructuring.

In general, nanostructured materials have a much lower thermal conductivity than bulk materials, due to phonon confinement and boundary scattering, especially in semiconductor nanowires (NWs) [1,2], leading to a higher figure of merit:

$$ZT = \frac{S^2 \sigma T}{K}. \quad (1)$$

Here S is the Seebeck coefficient, σ is the electrical conductivity, and K is the thermal conductivity (TC) of the material which includes both the lattice and the electronic contributions. To further improve this figure of merit, it is often accepted that the more efficient approach is to reduce the TC. To reach this objective, a series of geometrical and structural parameters of nanowires such as (i) size, (ii) surface structure, (iii) structural composition, and (iv) amorphization may be tuned by choosing different growth protocols and conditions. All of those modifications affect the transport properties [3]. For example, the decrease of the Si nanowires cross section leads to a ZT close to 1, which is already a rather large value compared to most bulk materials. To increase ZT beyond

this value, new nanostructured materials must be designed. Surface roughness of the nanowires can also significantly increase the figure of merit [4,5]. Following this idea, an engineered surface roughness, as a diameter modulation of the nanowires, can also control the decrease of the thermal conductivity [6,7]. Such cross-section modulated nanowires are interesting candidates for thermoelectric applications [8,9], and during the last two to three years an increasing interest appeared in the literature [7,10,11]. This class of nanowires has already been achieved [12,13]. On the other hand, the addition of amorphous regions around or inside the latter ones can also have an important impact on their TC. This has been shown in a couple of studies for the case of crystalline-core/amorphous-shell NWs [3,14–16].

In this paper, we explore how the combination of nanostructuring and partial amorphization may be used to reduce the conductivity of silicon nanowires. To the best of our knowledge, this is the first illustration of a systematic study of several parameters combining the two effects. Furthermore, we have studied the thermal properties of a new class of nanowires, made of an amorphous core and a crystalline shell (*ac/cs* NWs). The fabrication of the *ac/cs* NWs has been reported very recently in the case of $\text{Ge}_2\text{Sb}_2\text{Te}_5$ core/shell nanowires [17] and also has been observed by V. Lysenko in small scale during heavy-ion bombardment of a forest of crystalline silicon nanowires [18,19]. We address our theoretical study through molecular dynamics (MD) simulations. The latter technique has been successfully applied in recent years to the study of thermal transport across semiconductor nanowires [3,6,20]. Besides, it has the advantage to provide information on the local vibrational properties of the nanowire.

The paper is structured as follows: in Sec. II we detail the MD methodology and the modeled structures. Section III is divided in three subsections that address each kind of studied nanowires. We first consider the impact of the diameter modulation on the TC of pristine crystalline silicon nanowires and we investigate the influence of the periodicity of the modulation. In the second subsection, the impact on the TC of an amorphous phase in constant cross-section core/shell

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silicon nanowires is evaluated, by varying the amorphous phase fraction. In this subsection, two types of core/shell nanowires are presented: (i) the most common type of *a-Si/c-Si* nanowire, which is the crystalline-core/amorphous-shell (*cc/as*) and which can be realized through the interaction of a crystalline structure with the oxygen or caused by ion irradiation [18], and (ii) amorphous-core/crystalline-shell nanowires (*ac/cs*), which can be elaborated by a swift and spatially resolved heavy-ion bombardment [18]. Finally, in the third subsection we report the TC of *cc/as*, which are also diameter-modulated silicon nanowires for three different configurations. Then, a summary and perspectives to this work are given in the concluding paragraph.

II. MOLECULAR DYNAMICS

A. Nonequilibrium molecular dynamics method and simulation setup

We used the nonequilibrium molecular dynamics method [21,22] to compute the TC of modulated silicon nanowires. For such simulations, when steady state is reached, the temperature profile along the nanowire length and the heat flux through the structure are extracted. Assuming diffusive heat transport, which is the case here due to strong phonon scattering in nanowires, and applying Fourier's law, one can calculate the TC [22–27]. For each modeled structure, the TC of four nanowires with different lengths was calculated in order to extrapolate the TC corresponding to an infinite length [28], while the phonon mean free path has been estimated with the equation $1/l_{\text{eff}} = 1/l_{\infty} + 4/L_z$ [23], where L_z is the length of the nanowire from the hot to the cold thermostat and l_{eff} is the phonon mean free path. This estimation provides the average phonon mean free path, which is the mean free path of energy modes that carry heat and contribute to the thermal conductivity. The MD code LAMMPS [29,30] is used for our calculations.

In what concerns boundary conditions, periodic ones are set in the z direction, which is the main axis of the nanowires, while in the x and y directions we use free boundary conditions [20]. At the extremities of the nanowires, fixed atoms within regions of 1-nm width are set in order to avoid any movement of the nanostructures. Alongside the fixed atom regions, we set the hot and cold thermostats, with length of the thermostats 1.086 nm ($2\alpha_0$). Thermostating is performed through rescaling of the velocities of the atoms belonging to the group of the heat sink and heat source, for each time step. They induce a heat flux within the system. For all simulations the mean temperature was 300 K, while the heat source was at 330 K, and the heat sink was at 270 K. The time step used is 5 fs. The nanowire lengths vary from 13.3 to 49.3 nm. The classical Stillinger-Weber potential for the pure crystalline system is used [31], while its modified version, the so-called SW-VBM [32], is employed for all *a-Si/c-Si* nanowires. In Fig. 1, a characteristic temperature profile of a diameter-modulated crystalline-core/amorphous-shell silicon nanowire is displayed. The atoms are colored by their mean temperature for a simulation run with a duration of 5 ps, which is enough here to obtain a linear temperature profile.

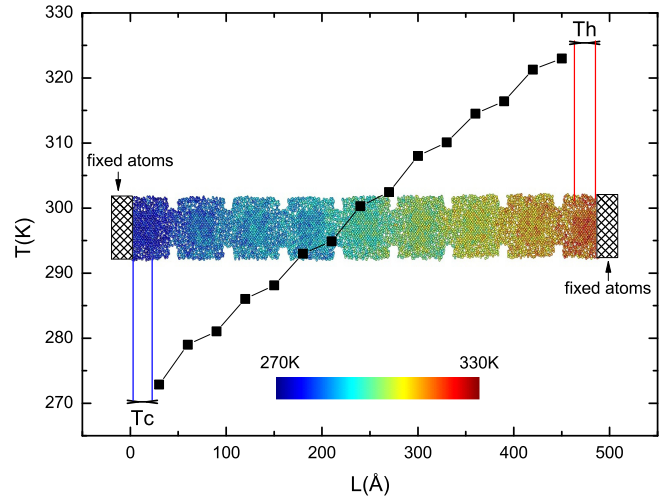


FIG. 1. (Color online) Temperature profile of a diameter-modulated nanowire with crystalline nano-inclusions. The atoms are colored by their mean temperature. The temperature profile exhibits different slopes corresponding to the diameter modulation effect on TC.

B. Modeling of core/shell nanowires

In the present work an important issue is to build realistic amorphous/crystalline structures. Here, we used the method presented in previous papers [33,34] and that has been already applied in the case of amorphous/crystalline superlattices [35] to determine the atom location in the nanostructures. This technique is achieved in two main stages which involve several steps. The first stage is the creation of a realistic silicon amorphous slab and the starting step is to define a random distribution of atoms inside a simulation box with the desired dimensions. Then, the system is annealed using a Nose-Hoover thermostat [36] at 2400 K, during 50 ps in the $N-V-T$ ensemble. The next step is the quenching of the system, with a rate of 10^{12} K s^{-1} . Finally, a global relaxation in the isothermal isobaric ensemble ($N-P-T$) is performed. It is important to notice at this point that the potential used for this “first stage” is the modified Stillinger-Weber potential (SW-VBM) for amorphous silicon [32]. It allows us to obtain a realistic amorphous phase. Once the slab is generated, the quality of the amorphous phase is checked using the radial distribution function, which is compared with x-ray data [37,38], the coordination number [38], the bond angle, and the bond length distribution [34]. We have checked the reproducibility of the amorphous phase, creating several amorphous slabs. They each have the same radial and bond distribution function, as well as coordination number. Thus, all the following nanowires which contain amorphous phase are fabricated with the same amorphous slab as the starting structure, assuming that no significant modifications of the thermal and vibrational properties can arise.

In the second stage of the amorphous/crystalline nanostructure elaborations, certain regions including several atoms are removed in order to create empty spaces within the amorphous slab. These empty spaces are then filled with the crystalline phase. In the following step, the system is annealed at 300 K to obtain a pressure near 0 GPa, and finally the energy of the

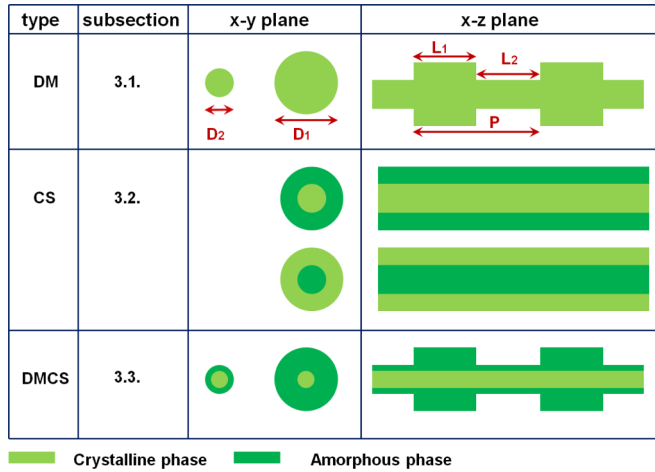


FIG. 2. (Color online) Schematic representation of the diameter-modulated crystalline silicon nanowires (DM), the *a-Si/c-Si* core/shell (CS) nanowires, and the *a-Si/c-Si* diameter-modulated core/shell (DMCS) nanowires.

system is minimized. Once again, at the end of this second stage we ensure that the build structure is correct. To do this, the quality of the interfaces in amorphous/crystalline structures is controlled with the atomic energy profile along the radial axis of the nanowires, starting from the center and going to the external radius [34]. The calculated interfacial energy 0.46 J m^{-2} [34] is compared with tight-binding results 0.49 J m^{-2} [39] and experimental ones 0.48 J m^{-2} [40], leading to an excellent agreement.

As it was mentioned in the introduction, we study three types of nanowires with different morphologies (see Fig. 2). The first ones are pristine crystalline nanowires with diameter modulation (labeled DM). The second class is of the core/shell type nanowires (labeled CS), with constant diameter. Among them there are two subtypes: the *cc/as* and the *ac/cs*. The considered external diameter for these nanowires is 6 nm and for this case nanowires with several ratios of the crystalline/amorphous phases are created by varying the core radius. Finally there are the diameter-modulated *cc/as* nanowires, where three distinctive configurations are considered (labeled DMCS, Fig. 2): (i) the C1 configuration, in which the diameter of the crystalline core remains constant and where only the amorphous shell diameter is modulated; (ii) the C2 configuration, in which both the core and the shell diameters are modulated; and (iii) the C3 configuration, consisting of crystalline nano-inclusions inside a diameter-modulated amorphous shell.

The CS and DMCS nanowires were constructed with the method described above for the amorphous phase integration. We modeled all types of nanowires with a circular cross section.

III. THERMAL CONDUCTIVITY OF SILICON NANOWIRES

A. Diameter-modulated crystalline silicon nanowires (DM)

The size reduction and the surface roughness of nanowires have been reported as major parameters to reduce their

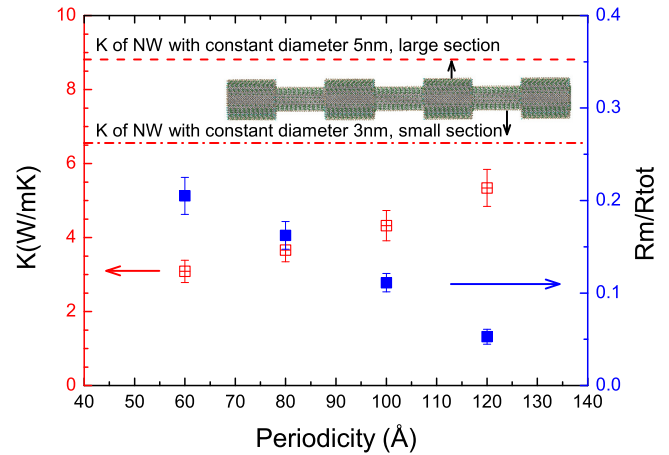


FIG. 3. (Color online) Evolution of TC and of the ratio R_m/R_{tot} as a function of the periodicity P of the diameter modulations.

TC [5,41]. A recent work on diameter and lattice modulated SiC nanowires [6] showed an important decrease of the thermal conductivity compared to the one of nanowires with a constant diameter. In this section we investigate the impact of the diameter modulation of crystalline silicon nanowires on the TC. The free parameter in our study is the periodicity P of diameter modulation in the direction of the nanowires axis (see Fig. 2). The latter one is in the range $60 < P < 120 \text{ \AA}$. For these nanowires, the small radius is $D_2=3 \text{ nm}$, and the large radius is $D_1=5 \text{ nm}$ and the corresponding lengths are $L_1 = L_2 = P/2$ (Fig. 2 top).

For all periodicities, we report in Fig. 3 the calculated TC of nanowires extrapolated to infinite size [28]. We give also the values of the thermal conductivity of a constant diameter silicon nanowire with 3- and 5-nm diameter for the sake of comparison. The temperature jump across the constrictions is computed, and we report the thermal constriction resistance [6], defined as $R_m = \frac{\Delta T}{\varphi}$, where ΔT is the temperature jump at the interface, and φ is the heat flux through the structure. K and the ratio R_m/R_{tot} are plotted in Fig. 3, where R_{tot} is the total thermal resistance of the whole structure. Our results show the same tendencies as those previously reported for such nanowires [6,10]. The diameter modulation allows us to reach a lower TC than the one of the smaller constant diameter nanowires due to the additional scattering mechanism from the constrictions which behave as scattering centers. The qualitative results in this study are owing to the influence of the periodicity of the diameter modulation. When the periodicity decreases the TC decreases, even by a factor of 2. Decreasing the periodicity is equivalent to increasing the constriction concentration x , and therefore the scattering probability of heat carrying phonons, or inverse relaxation time $1/\tau$. Indeed the linear behavior observed in Fig. 3 suggests that $1/\tau \propto x$, as for point scattering. The ratio R_m/R_{tot} shows a clear decrease as P is increased. This is however not surprising due to the change in the interface/volume ratio as P is increased since R_m is calculated in a fixed volume. The value of R_m itself does not change with the periodicity and it is equal to $3.3 \times 10^{10} \text{ m}^2\text{K/W}$. This confirms our suggestion

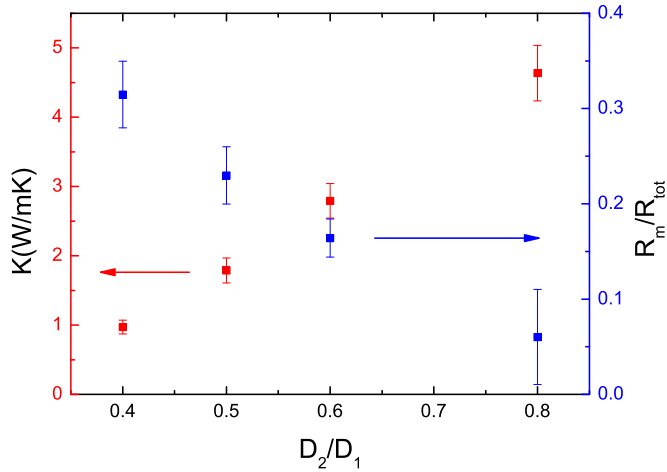


FIG. 4. (Color online) TC and thermal constriction resistance ratio R_m/R_{tot} of single nanoconstriction as a function of the diameters ratio D_2/D_1 .

to consider the constrictions as independent scattering centers, not interacting with each other in that range of periodicity.

To get complementary information about the behavior of the thermal conductivity we have studied a single nanoconstriction, which we have seen can be considered as an independent scattering center. It was modeled setting a thin diameter nanowire between two “bulklike” reservoirs. The latter ones have a constant diameter $D_1 = 5$ nm and constant length $L_1 = 10$ nm. The characteristic lengths of the “thin nanowire” are its constant length $L_2 = 5$ nm and its diameter D_2 which can vary from 2 to 4 nm. Both nanowire and reservoirs have a circular cross section. Here, the size of the reservoirs is limited because periodic boundary conditions cannot be simply applied to a part of this cylindrical nanostructure. Thus, increasing the number of atoms in the reservoir drastically increases the computational time to reach convergence. Besides, we must also check that the thermostats, set in reservoirs, are far enough from the nanoconstriction. By varying the nanowire diameter D_2 , we succeed to vary the ratio D_2/D_1 and finally study the effect of the height of a nanoconstriction on the TC and the thermal constriction resistance ratio R_m/R_{tot} . These results are depicted in Fig. 4.

It can be noted that the thermal constriction resistance ratio decreases significantly when the D_2/D_1 ratio increases. This means that the more D_1 and D_2 are different the smaller the thermal conductivity will be. Our results are consistent with those obtained by He and Galli [16], showing that increasing the depth of the ripples in rough nanowires lowers the TC of the nanostructure. In their work this behavior was attributed to an increase of the free surface scattering.

To further understand the resistive effect of the modulations which are material dependent, we compute the VDOS (see Fig. 5) which is obtained through a Fourier transform of the time velocity correlation functions [42,43]. The VMD module [44] was used to compute both the autocorrelation and Fourier’s transforms. Specific regions of the system have been chosen, with widths of 4 Å, which are localized at each side of the nanoconstriction. The names for these regions are defined in Fig. 6.

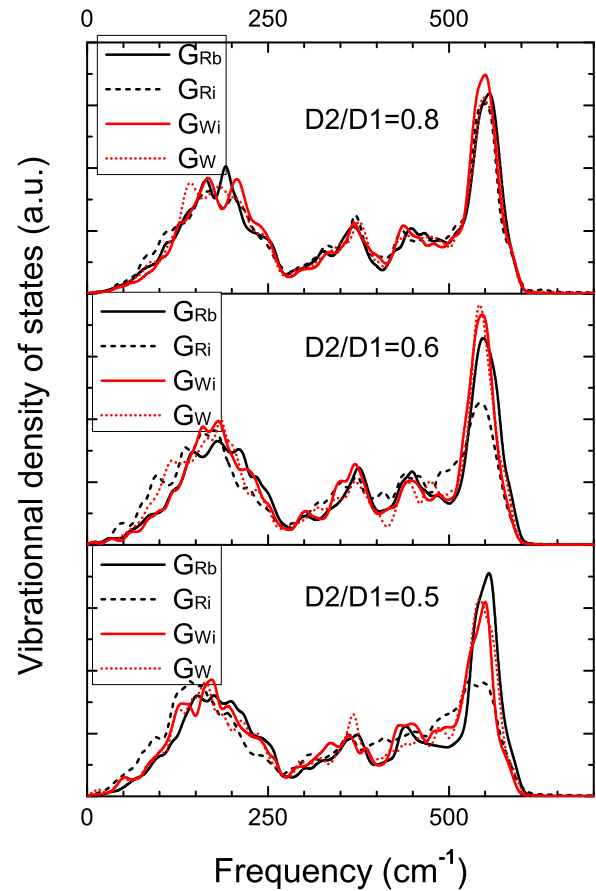


FIG. 5. (Color online) VDOS inside the four regions G_{Rb} , G_{Ri} , G_{Wi} , and G_W , for three diameter ratios of the nanowires ($D_1/D_2 = 0.8$; $D_1/D_2 = 0.6$; $D_1/D_2 = 0.5$), using the SW interatomic potential [31].

The VDOS of the group of atoms far from the nanoconstriction (G_{Rb}) is represented with a black line in all three panels. This is essentially the VDOS of bulk silicon. The black dashed lines correspond to the VDOS of a group of atoms in the reservoir but close to the nanoconstriction (G_{Ri}). The three subfigures have been obtained for three different ratios of D_2/D_1 . One can observe a systematic decrease of the main peak of the G_{Ri} as D_2/D_1 decreases for the three configurations. The latter one corresponds to the transverse optical mode (TO) at frequency 550 cm^{-1} . One can also observe a softening of the acoustic phonons, say those with a

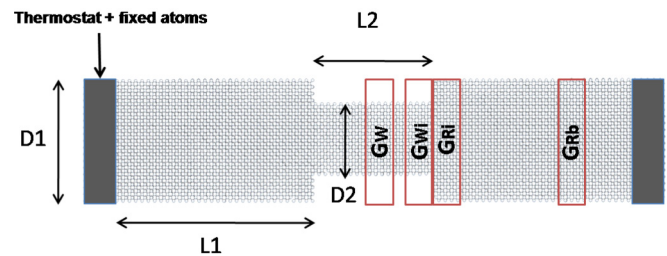


FIG. 6. (Color online) Schematic representation of a crystalline single nanoconstriction and its characteristic lengths. The red rectangles represent the groups of atoms used to extract the VDOS.

frequency below 200 cm^{-1} . Considering that these phonons are those which carry more heat, we can understand that this softening is another mechanism that will contribute to thermal conductivity reduction, in addition to the direct effect on scattering provided by the geometry of the nanoconstriction.

Let us focus now on the VDOS of the other two groups (Gw) and (Gwi) (Fig. 5) which are defined in Fig. 6. These atoms are located in the thin nanowire, far and close to the constriction, respectively. One can also observe a softening of the phonon frequencies, but it is now less meaningful than in the nanowire with the large diameter. Nevertheless, it is now clear that phonon softening should be included for a qualitative description of the thermal conductivity reduction in diameter-modulated nanowires. Besides, it has been observed that even small scale phonon softening can be attributed to the reduction of the thermal conductivity in a variety of materials [45–47]. The phonon softening can originate from the existence of the free surfaces, the atoms close to the amorphous/crystalline interface, the confined structures, and the roughness when it is included in modeling. All of these structural modifications induce additional scattering, which affects the phonon propagation.

B. Core/shell silicon nanowires with crystalline and amorphous phases and constant diameter (CS)

In this section the TC and the VDOS of two types of core/shell nanowires are given: (i) first for the usual crystalline core and amorphous shell (cc/as) nanowire and (ii) second for an amorphous core and crystalline shell (ac/cs). The external diameter of both types of nanowires is kept constant with a diameter equal to $D_2 = 6\text{ nm}$. We modified the core nanowire radius (internal interface) to study the impact of the amorphous fraction to the crystalline phase in both configurations. In Fig. 7, the TC of the two types of core/shell nanowires is given as a function of the amorphous fraction to the total amorphous and crystalline phases. Results for the TC of pristine crystalline (0%) and pristine amorphous (100%) nanowires are also given for the sake of comparison.

As intuitively expected in all cases the thermal conductivity decreases with the increase of amorphous content. The discrepancies between our study and the results obtained by Sansoz [15] are small and in absolute values should be related to the differences existing on the diameter of the cross sections ($D_2 = 6\text{ nm}$ in our case and $D_2 = 9.8\text{ nm}$ in Sansoz's work), the shape of the cross sections (circular in our case and hexagonal in Sansoz's work), and probably the interatomic potential (SW-VBM in our study and not specified in Sansoz's case).

Concerning the cc/ac system, other groups have carried out similar studies [3,14]. They also calculate the TC of such nanostructures. However, we believe that the use of the Tersoff potential instead of the SW-VBM (like in our case) and the very small cross sections, that they have considered, cannot correctly reproduce the TC of the studied nanowires. In [14], they reported a decrease of the TC by a factor of 20, when they add an amorphous shell around a crystalline core, compared to the same size crystalline nanowire. It seems large, and we have to mention that the chosen method to prepare the amorphous/crystalline interfaces is especially crucial for

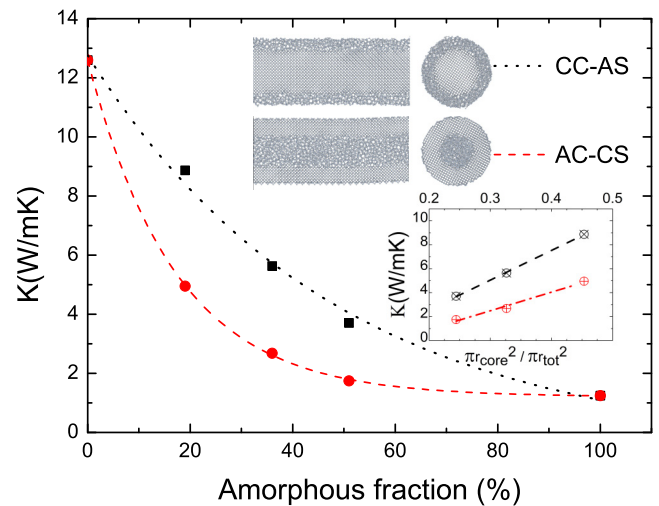


FIG. 7. (Color online) Evolution of the TC of the two types of core/shell amorphous/crystalline silicon nanowires as a function of the amorphous phase fraction (the dashed and dotted lines serve as a guide for the eye). The TCs of the pristine crystalline (0%) and the pristine amorphous (100%) silicon nanowires, with the same cross section, are also given. Inset: The thermal conductivity for the two types of nanowires as a function of the ratio of the cross-section area of the core and the total cross-section area (the dashed lines here are linear fittings of the data).

the assessment of transport properties. The melting/quenching technique combined with an inadequate potential for amorphous silicon can create large voids and mechanical stresses at the interfaces. This will affect the resulting TC. Our methodology to generate amorphous/crystalline interfaces takes into account experimental results as the interfacial energy or the radial distribution function to ensure the quality of both the amorphous phase and the amorphous/crystalline interface at an atomic scale [34]. We believe that the discrepancies between the different studies are first due to the way the core/shell nanowires are modeled and second due to the potential which is used for MD simulations. The latter one plays an important role for the building of the amorphous slab and it is known that the Tersoff potential has a tendency to induce a higher coordination number than the SW or the SW-VBM potentials [34].

Concerning the new class of core/shell nanowires, the one with a crystalline shell and amorphous core (ac/cs), we show that it exhibits very interesting properties. First of all, the impact of the amorphous phase fraction on the overall TC follows the same tendency as before; when increasing the amorphous fraction the TC decreases. On the other hand, the TC of the ac/cs nanowires is half compared to the cc/as for the same fraction of the amorphous phase. This might be exploited further for applications in thermoelectric devices. The reduced TC can be rationalized if one assumes that the thermal transport occurs mainly at the crystalline part. In that case the scattering probability of the phonons in crystalline regions, and therefore the thermal resistivity, will be proportional to the surface scattering area. The surface area is larger in the ac/cs system due to the presence of both external and internal boundaries, which would explain the lower thermal conductivity.

The inset in Fig. 7 shows the linear relation between the TCs for both types of nanowires and the ratio of the cross-section areas of the core and the whole nanowire. We can conclude that there is a geometric parameter that scales the TC when the core cross section varies. We also tried to interpret the drop of the conductivity of core/shell nanowires using a Monte Carlo approach, where phonons were allowed to propagate and be scattered by boundaries. It turned out that this simple model gave a thermal conductivity that decreases linearly with the amorphous volume fraction, and which does not depend on the position of the amorphous region (if it is at the core or at the shell). This suggests that indeed energy transfer in amorphous silicon cannot be described by collective propagative excitations, and the peculiar behavior outlined by the molecular dynamics simulations can be ascribed to the diffuse nature of the energy carriers in amorphous regions.

To get a more microscopic picture we also studied the VDOS for the *cc/as* and *ac/cs* nanowires, separating the contributions of the crystalline and the amorphous regions. Results are presented in Fig. 8. For what concerns the optical peak the contribution of the amorphous phase to the VDOS is similar in both configurations. For the crystalline phase, when it is in the center of the nanowire, then it exhibits a large peak around 500 cm^{-1} characteristic of the crystalline behavior. This is no longer true when the crystalline phase is in the outer position (shell). In the latter case the surface

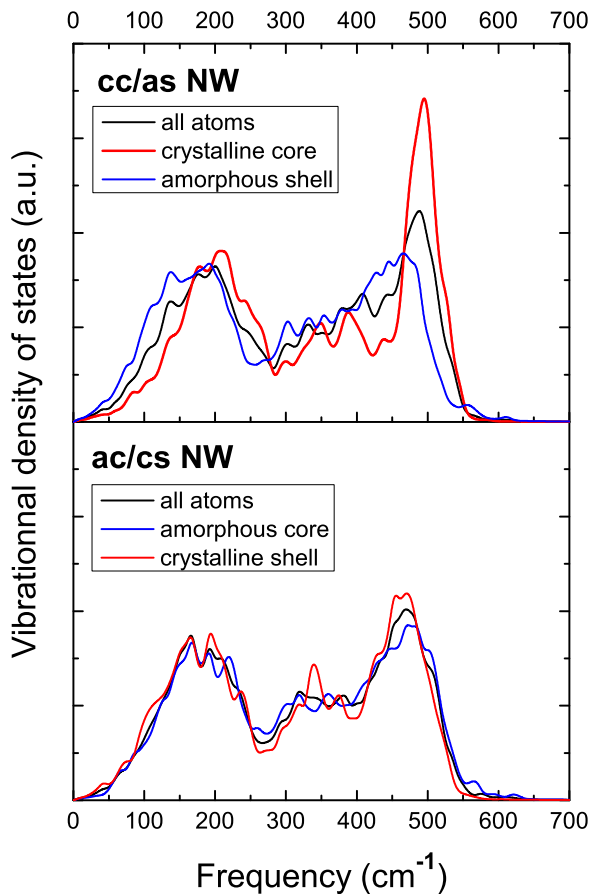


FIG. 8. (Color online) VDOS in *cc/as* and *ac/cs* nanowires, using the interatomic potential SW-VBM [32].

reconstruction modifies significantly the vibrational properties and the overall behavior is close to the amorphous phase. For the acoustic modes the behavior is different. In the *cc/as* system the VDOSs of the amorphous and crystalline regions are rather similar to the ones of bulk systems. There is a softening of the modes in the amorphous phase compared to the crystalline one. However in the *ac/cs* case the VDOS is shifted to an averaged intermediate value, rather similar in the crystalline and amorphous regions. Again we expect this behavior to be a consequence of surface reconstruction, which in turn contributes to reduce the thermal conductivity.

To confirm this behavior on the phonon eigenmodes, which are responsible for the transport properties rather than the distribution of phonon frequencies, we have also computed the participation ratio. It is a measure of the number of atoms participating to a given eigenmode α Eq. (2). It is defined by

$$P_\alpha = \frac{1}{N} \frac{(\sum_{I=1}^N |u_\alpha^I|^2)^2}{\sum_{I=1}^N |u_\alpha^I|^4} \quad (2)$$

if the N atoms are labeled by I and u_α^I is the eigendisplacement for mode α . If P atoms are participating equally to u_α^I , then $P_\alpha = P/N$. Now if we call $\rho_I(\omega)$ the projected density of states on atom I we can also define the function

$$p(\omega) = \frac{1}{N} \frac{[\sum_{I=1}^N \rho_I(\omega)]^2}{\sum_{I=1}^N \rho_I(\omega)^2} \quad (3)$$

and we have the result that at the phonon eigenfrequency ω_α , $p(\omega_\alpha) = P_\alpha$ Eq. (3).

In the following we prefer to plot $p(\omega)$ rather than P_α since the local densities of states $\rho_I(\omega)$ are usually available from the output of molecular dynamics calculations, whereas the phonon eigenmodes (u_α^I) are not. The results are presented in Fig. 9. As expected in the optical region the amorphous phases are very similar in all configurations. In the acoustic part of the spectrum the *cc/as* and the bulk phases are very similar, the eigenstates being more localized in the amorphous phase. It is however interesting to remark that what has been said for the frequency distribution remains true for the localization of the eigenstates: in the *ac/cs* they are averaged to a value intermediate between the crystalline and the amorphous values of the bulk phases, and the core and shell parts of the nanowire have quite similar properties. This reveals that the phonon eigenvectors are fundamentally different in the *ac/cs* due to surface reconstruction, which explains the drastic reduction of the thermal conductivity observed in Fig. 7. Strictly speaking there is no facet, when there is an amorphous phase at the shell of the nanowires. In the case where the crystalline phase is at the outside, the reconstruction mainly follows the (100) and the (110) facets and between them the (520) facets.

To conclude this section, that includes the most important results in the sense of a new class of nanowires, we should mention that there is a series of reasons to explain the difference in the thermal conductivity between the *cc/as* and the *ac/cs* NWs. First of all phonons that carry heat (mainly in the crystalline phase) in the *ac/cs* configuration are scattered both at the interface between the amorphous core and the crystalline shell and at the free surface that has been reconstructed. In the classical case nanowires (*cc/as*) there was only the internal interface as scattering center. Second,

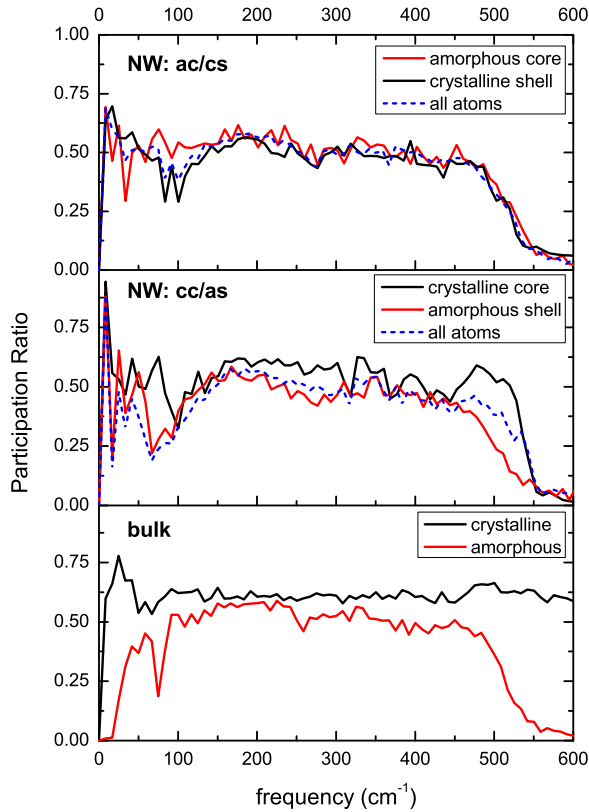


FIG. 9. (Color online) Participation ratios in the *cc/as* and *ac/cs* configurations as well as in the bulk.

we calculated the phonon mean free path in the classical case to be 5 nm, while in the *ac/cs* NWs version it is divided by a factor of 2: 2.7 nm. These results are for the same amorphous phase fraction in the two cases, corresponding to 19%. Moreover, with the help of the VDOS, we can see that in the *ac/cs* case the DOSs of the amorphous and crystalline parts are both similar to the amorphous bulk phase, while in the classical case the crystalline core preserves the DOS of the bulk crystalline phase. This confirms that the phonons feel the interface between the amorphous and the crystalline phases not as a sharp interface, but rather like a broad boundary with a thickness of 2 nm. This has been also observed in a previous study [35] in the case of superlattices with crystalline and amorphous phases, where the DOS in the crystalline phase becomes similar to the one of bulk crystalline silicon in a distance of almost 1 nm from the amorphous/crystalline theoretical interface.

C. *cc/as* diameter-modulated silicon nanowires (DMCS)

As showed in the previous subsection, when the amorphous fraction is adequate the TC is reduced, by a factor of roughly 6 in the case of the core/shell nanowires compared to those of pristine crystalline nanowires. In this last section we consider a simple model of nanowire which could be relevant for thermoelectricity. For this purpose the electronic properties should be preserved as much as possible while the thermal conductivity is reduced. We should mention at this point that one expects a preservation of the electronic conductivity as

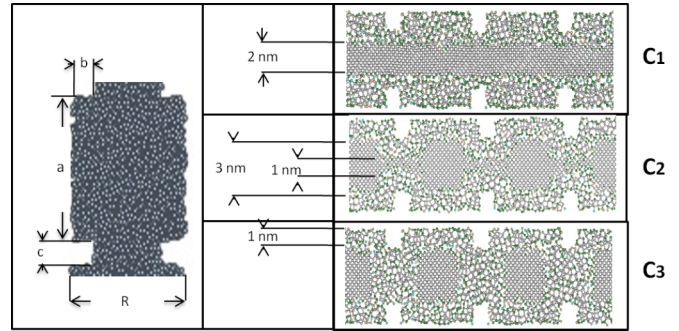


FIG. 10. (Color online) Snapshots of the three configurations used to model the diameter-modulated *cc/as* silicon nanowires. (a) *C1* configuration, with constant diameter crystalline core and diameter-modulated amorphous shell. (b) *C2* configuration, where both crystalline and amorphous regions are diameter modulated. (c) *C3* configuration consisting of crystalline nanoinclusions inside a diameter-modulated amorphous shell.

the electron mean free path is much smaller than the one of the phonons [48,49]. For example, at room temperature and for electron doping of $\sim 10^{19} \text{ cm}^{-3}$, averaged mean-free-path values can be extracted from transport measurements [50,51] and are of the order of 1–10 nm for electrons and 40 nm for phonons. However in a recent paper by B. Qiu and Tian [52] it was shown that the differences could be much larger for the electron and the phonon modes contributing to the conductivities. This leads to the conclusion that the electrons should feel much less the structural modulations compared to the phonons. Of course one should calculate the electronic conductivity of these nanostructures, but this is not in the scope of our paper. We investigate now the diameter-modulated nanowires with crystalline core and amorphous shell (*cc/as*). The three configurations we consider are (see Fig. 10) (i) the *C1* configuration in which there is a continuous and constant diameter crystalline core with an amorphous modulated shell; (ii) the *C2* configuration in which both the crystalline core and the amorphous shell regions are diameter modulated and for which there is a continuous crystalline axis in the center of the nanowire; and (iii) the *C3* configuration in which there are crystalline nanoinclusions inside a diameter-modulated amorphous envelope. The TC of these three diameter-modulated core/shell nanowires is given in Fig. 11, and to achieve comparisons we added the results of the pristine crystalline and pristine amorphous diameter-modulated nanowires with the same size as the *C1*, *C2*, and *C3* configurations. To add to the discussion previously mentioned about the electronic conductivity, we are quite confident that in the case of the *C1* configuration, as there is no additional roughness at the interface between crystalline and amorphous phases, the electronic conductivity should be equal to the one of a pristine crystalline nanowire with diameter equal to the core diameter [53]. An additional roughness like in cases of the configurations *C2* and *C3* would deteriorate the electronic conductivity, and finally we cannot give a clear statement for the benefit to the overall figure of merit.

In Fig. 10 we observe that along the series *C1*, *C2*, and *C3* the central crystalline channel is being enclosed by the amorphous phase. The results for the thermal conductivity presented in

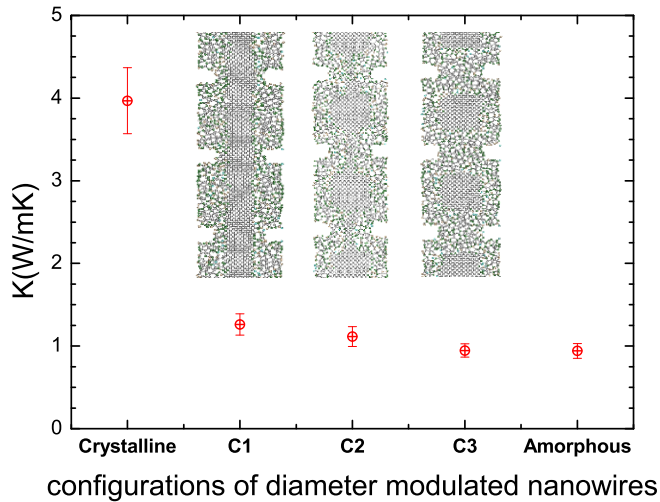


FIG. 11. (Color online) TC of the three diameter and phase modulated nanowires (DMCS) as presented in Fig. 10, compared with the pristine crystalline and pristine amorphous diameter-modulated nanowires with the same dimensions.

Fig. 11 show that it is reduced further and further along the series. The values for the C_2 and C_3 configurations are in fact very close to the one of the modulated nanowires with only amorphous phase. In the case of thermoelectricity we believe that C_2 is the most favorable one, since electronic transport should be preserved more compared to the C_3 configuration. In fact those results are complementary to the ones discussed in a previous work by He and Galli [16]. They studied DMCS nanowires with a larger diameter (15 nm instead of our 5 nm), but much shorter. They found that the TC for DMCS a -Si/ c -Si nanowires equals to 25% of the constant section crystalline nanowire. Here, we found a TC for the C_2 configuration equal to 17% of the crystalline constant section nanowire.

IV. CONCLUSION

Here we presented models to lower, as low as the amorphous limit, the TC of silicon nanowires. The combined incorporation of the amorphous phase and the diameter modulations is a way to decrease the TC, without totally suppressing the electronic conductivity. Therefore, such design could increase the thermoelectric figure of merit of the nanowire. The ac/cs nanowires are very interesting candidates for such purpose as they exhibit TC of half of the usual core/shell nanowires, and without diameter modulation, which is experimentally difficult to achieve. The addition of modulations and amor-

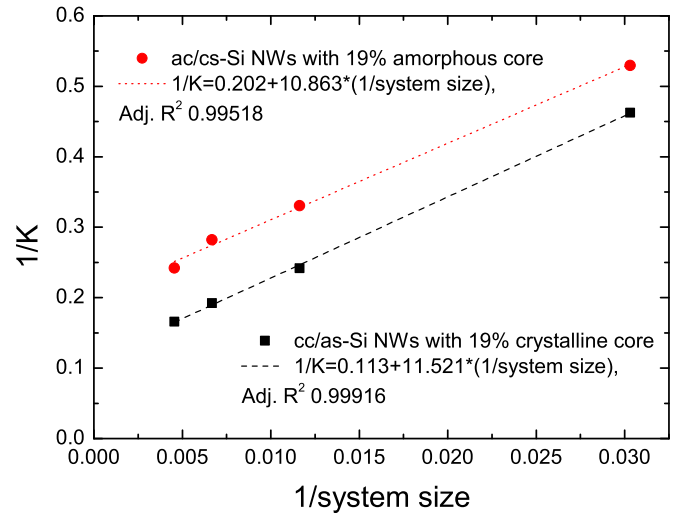


FIG. 12. (Color online) Inverse of the thermal conductivity vs the inverse system size.

phous/crystalline structures can lead to a TC close to the one of the pristine amorphous nanowire, while preserving a small portion of crystalline material in the core of the nanowire. The latter issue could be crucial for the transport of electrons. Further investigation is however necessary to confirm this statement.

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APPENDIX

In Fig. 12 the inverse of the thermal conductivity as a function of the inverse system size in the direction of the heat flow is given. The two types of core/shell NWs are given; the cc/as and ac/cs nanowires are both with the same percentage of amorphous phase. In the inset the extrapolation equation and the statistical error of this extrapolation are given. The R square (square of the correlation between the response values and the predicted response values) measures how successful the fit is, in explaining the variation of the data.

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