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Flatness Based Grey Wolf Control for Load Voltage Unbalance Mitigation in Three-Phase Four-Leg Voltage Source Inverters

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Abstract -- Standalone power-supply systems become a key solution to effectively address load demand in remote locations wherein the voltage asymmetry raises as a particular concern in view of the large number of single phase household and communities loads. Over these loading conditions, the use of Four-Leg Voltage Source Inverters (FL-VSIs) appears as a suitable topology to provide clean power with symmetrical voltage waveforms. The reported work employs the differential flatness theory associated with Grey Wolf optimization in order to guarantee proper operations of the FL-VSIs in different loading conditions. In fact, the differential flatness theory is applied to check the flatness of the FL-VSIs, which allows implementing a reduced control model. Then, a Grey Wolf algorithm which acts as a tracking controller (GWC) is developed. The GWC generates an optimal control signal used by the differentially flat model so as to ensure adequate control performance, even under severe load disturbances and model inaccuracies. A comprehensive experimental test is carried out to validate the effectiveness of the proposed flatness based grey wolf controller (FB-GWC).

Index Terms-- Standalone Microgrids, Voltage unbalance, Four-Leg Voltage Source Inverter (FL-VSI), Differential flatness, Grey Wolf optimization.

I. INTRODUCTION

The output voltage quality in standalone power-supply systems is gaining more attention with regards to the increasing penetration of distributed renewable energy resources [1][2]. However, the integration of such power sources as distributed generation entities must to comply with many technical and performance requirements [3-5]. Accordingly, a number of regulatory and grid considerations refer to power quality in standalone power-supply applications. Among them, European standard EN 50160 states that the allowable variation of supplying voltages cannot exceed 10% of their nominal values. The voltage harmonic content quantified by the total harmonic distortion index (THD) must, according to IEEE standard 519-1992 and recently 1547-2014, not exceed 5%. Furthermore; the voltage

unbalance factor (VUF) should be in compliance with the IEC standards and kept below 2% as long as the consumers' current does not include which essentially characterizes the prevalence of single-phase loads in three-phase four-wire electrical networks [6-12]. Indeed, the unbalanced loading can represent a large extent of the total load scenario in standalone microgrid configurations wherein the voltage asymmetry raises as a particular concern in view of the large number of single phase household and communities loads [13][14].

In this context, the use of FL-VSIs as power source conversion units or even as power quality conditioners appears as a suitable topology to maintain balanced sinusoidal output voltage waveforms over all loading conditions in transformer-less applications [15]. Compared to three phase three leg inverters with split DC-links, the addition of the fourth leg provides numerous extra advantages in terms of voltage rating. Indeed, reduced DC bus voltage is involved and larger AC voltage may be supported (over 15%) since it requires a reduced DC bus voltage and can support larger AC voltages (over 15%) [10]. Moreover, the additional fourth leg provides a path to draw zero current sequences, which sustains the converter capability to handle critical load conditions, such as the presence of single-phase or unbalanced phase loading [16]. FL-VSIs are widely used in different standalone and grid connected applications, namely distributed generation [17] with grid connected [18], standalone [13] and uninterruptible power supply mode of operation [19]: active power filtering [20-22], including distributed static compensators [23] and dynamic voltage restorers [24].

Nevertheless, the control of such converter in standalone power-supply applications represents a quite complicated task and, therefore, an advanced control strategy has to be designed. The control strategy synthesis must have three main objectives, tracking performances, disturbance rejection

and load voltage unbalance compensation. For this purpose, many control schemes have been previously analyzed in different reference frames. They focus on control of load voltage and must meet power sharing and quality requirements in power applications based FL-VSIs under unbalanced and various load conditions. In this way, a number of research contributions proposes classical control strategies, with proportional integral (PI) and proportional resonant linear regulators [23][25]. The obtained test results demonstrates the effectiveness and the simplicity of these controllers. However, their dynamic responses show sluggish performances in transients modes even more when cascaded control schemes are employed [26-28]]. An undesirable feature of these control schemes is the introduction of a decoupled control, which increases the control complexity, and correspondingly the computational load [15][25]. To deal with linear regulators limitations, several nonlinear control methods are proposed in the literature including feedback linearization control [21][29], backstepping [20][30] and sliding mode control [19]. These control methods use differentiation to achieve the control law, which results in high order derivative control terms. These derivatives can affect the control law capabilities under severe load disturbances and model inaccuracies.

Among the reported control strategies, the flatness-based control (FBC) appears as an interesting nonlinear control approach due to its useful performances when explicit trajectory planning is required under different operating conditions [31]. Indeed, the flatness theory allows an accurate description of the transient and the steady state dynamic of the overall system state variables based on differentially flat outputs. In addition, and as reported in various works [32-36], the FBC demonstrates stable performances even under large operating point and system parameters variation. For instance, the use of FBC has been proposed for three phase three leg VSIs and its effectiveness has been highlighted through comparisons with both proportional integral and feedback linearization controllers [34]. Also, the control of parallel three-phase VSIs is proposed in [35], where the flatness theory is used with the aim to minimize the effect of circulating currents and reduce voltage distortion at the Point of Common Coupling (PCC). A literature review of the main properties and applications of the FBC in power systems is proposed in [36].

More recently, optimization based control methods like model predictive controllers (MPC) and optimal linear quadratic regulators (LQR) emerge as effective tools [31][32]. For instance in [31], a MPC strategy is proposed for FL-VSI, wherein the minimization of a cost function yields the inverter optimal switching state to apply at the next sampling instant. This strategy presents relevant

performances, but suffers from a variable switching frequency. Moreover, the fourth leg operates at higher switching frequency than the phase legs, which complicates the output filter design as well as the neutral inductor sizing. In [32], a Particle Swarm Optimization (PSO) algorithm is proposed to on-line adapt the weighting matrices (Q and R) of an LQR controller. Besides the interesting performance of the proposed strategy, it can be mentioned that under severe nonlinear condition, the PSO technique can fall into local optimum, which can alter the control performances.

In this paper, a novel control strategy is proposed for FL-VSI based standalone power supply systems. The proposed strategy combines the advantages of flatness control and optimization. Indeed, a FBC controller is designed and extended with an evolutionary search algorithm that acts as a tracking controller to achieve the desired power quality requirements. The use of a Grey Wolf (GW) algorithm is preconized since it presents several advantages for practical implementation in terms of low computing complexity, and convergence accuracy [37]. The GW algorithm is used to search for an optimum correction of the flat output references in such a way to secure the desired trajectory tracking performances especially in the presence of substantial nonlinear and/or unbalanced loads.

The main contributions of this work are summarized as follows.

- 1) A FBC is extended with a GW optimization algorithm to enhance the control performance of an LC-L interfaced FL-VSI in presence of disturbing loads such as single-/three- phase, unbalanced, and nonlinear loads.
- 2) The GW algorithm depends on few parameters (weighting factors) that are tuned in order to minimize the voltage THD and VUF. The GW algorithm is used to minimize a cost function and generate the convenient control correction signal.
- 3) The robustness of the proposed controller is studied for large load disturbances and model parameters variations.

This paper is organized as follows. In Section II, the model of the studied system is presented, and, in Section III, the design methodology of the proposed FB-GWC is detailed. Section IV present the experimental results and discuss the effectiveness of the proposed approach. Finally, section V underlines the contributions of this work.

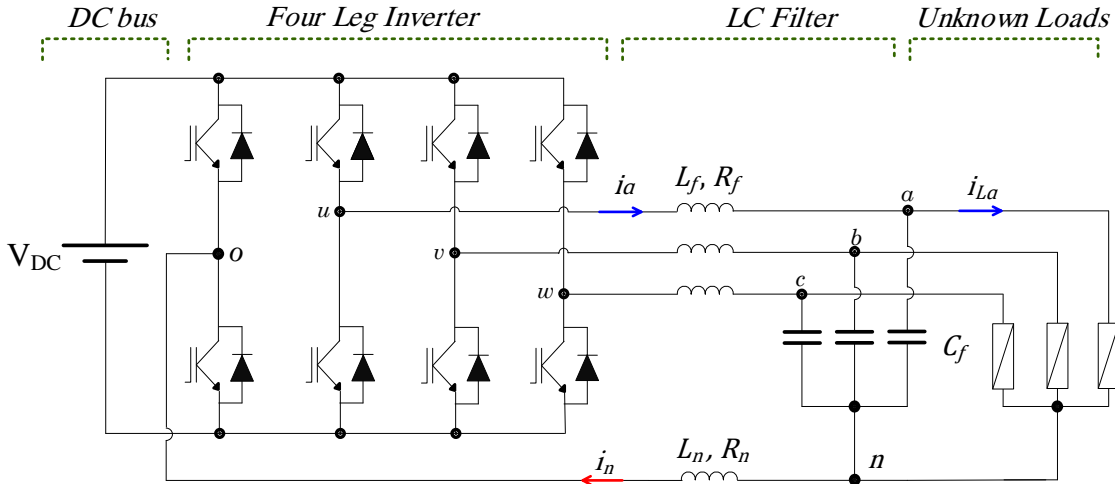


Fig. 1. Four-leg inverter topology with an output LC filter and a neutral line inductor.

II. SYSTEM DESCRIPTION AND MODELING

The typical circuit configuration of FL-VSIs with an output LC filter stage is depicted in Fig. 1. The DC-link is assumed as a constant voltage source. A neutral line inductor L_n is used to connect the midpoint 'o' of the fourth leg to the neutral point 'n' of the load bus with the aim to the neutral line current ripples [14].

For the sake of simplicity, the following voltage and current vectors are defined:

Inverter voltages vector $\mathbf{v}_i = [v_{uo}, v_{vo}, v_{wo}]^T$, output voltages vector $\mathbf{v}_f = [v_{an}, v_{bn}, v_{cn}]^T$, line currents vector $\mathbf{i}_f = [i_a, i_b, i_c]^T$, neutral current vector $\mathbf{i}_n = [i_n, i_n, i_n]^T$ and load currents vector $\mathbf{i}_{L,abc} = [i_{La}, i_{Lb}, i_{Lc}]^T$.

Note: Matrices and vectors are denoted with bold letters.

By applying Kirchhoff's laws for voltage and current, to the nodes given in Fig. 1, the following load dynamics equations (1) and (2) can be obtained, where the filter line currents \mathbf{i}_f and the output voltage \mathbf{v}_f are considered as the state variables.

$$\mathbf{v}_i = \mathbf{v}_f + R_f \mathbf{i}_f + L_f \frac{d\mathbf{i}_f}{dt} + \left(R_n \mathbf{i}_n + L_n \frac{d\mathbf{i}_n}{dt} \right) \quad (1)$$

$$C_f \frac{d\mathbf{v}_f}{dt} = \mathbf{i}_f - \mathbf{i}_{L,abc} \quad (2)$$

After straightforward Park's transformation $P(\theta)$ the system state equations are obtained in the synchronous reference frame:

$$\dot{\mathbf{x}} = \mathbf{A} \mathbf{x} + \mathbf{B} \mathbf{u} + \mathbf{C} \mathbf{d} \quad (3)$$

with: $\mathbf{x} = [i_0, i_d, i_q, v_{c0}, v_{cd}, v_{cq}]^T$ the state variables vector. $\mathbf{u} = [u_0, u_d, u_q]^T$ is the $0dq$ -components of the input vector.

The $0dq$ -components of the load currents is defined by $\mathbf{d} = \mathbf{i}_L = [i_{L0}, i_{Ld}, i_{Lq}]^T$, and

$$\mathbf{A} = \begin{bmatrix} -\frac{R_f+3R_n}{L_f+3L_n} & 0 & 0 & \frac{-1}{L_f+3L_n} & 0 & 0 \\ 0 & \frac{-R_f}{L_f} & \omega & 0 & \frac{-1}{L_f} & 0 \\ 0 & -\omega & \frac{-R_f}{L_f} & 0 & 0 & \frac{-1}{L_f} \\ \frac{1}{C_f} & 0 & 0 & 0 & 0 & 0 \\ 0 & \frac{1}{C_f} & 0 & 0 & 0 & \omega \\ 0 & 0 & \frac{1}{C_f} & 0 & -\omega & 0 \end{bmatrix},$$

$$\mathbf{B} = \begin{bmatrix} \frac{1}{L_f+3L_n} & 0 & 0 \\ 0 & \frac{1}{L_f} & 0 \\ 0 & 0 & \frac{1}{L_f} \\ 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix}, \quad \mathbf{C} = \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \\ \frac{-1}{C_f} & 0 & 0 \\ 0 & \frac{-1}{C_f} & 0 \\ 0 & 0 & \frac{-1}{C_f} \end{bmatrix}$$

A carrier based PWM technique presented in [38], where the block diagram is shown in Fig. 2, has been considered in this work due to its performance and ease of implementation. The input voltage references of the three principal phases with respect to the fourth-leg are denoted respectively: v_a^{ref} , v_b^{ref} and v_c^{ref} . The fourth leg reference voltage named offset voltage is calculated with respect to the DC bus fictive middle point: v_n^{ref} . Then, the reference voltages ($v_{aM}^{ref}, v_{bM}^{ref}, v_{cM}^{ref}$), as illustrated in Fig 2, with respect to the fictive DC midpoint are defined as:

$$v_{xM}^{ref} = v_x^{ref} + v_n^{ref}, \quad x \in \{a, b, c\} \quad (4)$$

The offset voltage v_{oM}^* can be calculated by

$$v_n^{ref} = mid\left(-\frac{v_{max}}{2}, -\frac{v_{min}}{2}, -\frac{v_{max} + v_{min}}{2}\right) \quad (5)$$

where: $v_{max} = \max(v_a^{ref}, v_b^{ref}, v_c^{ref})$ and $v_{min} = \min(v_a^{ref}, v_b^{ref}, v_c^{ref})$.

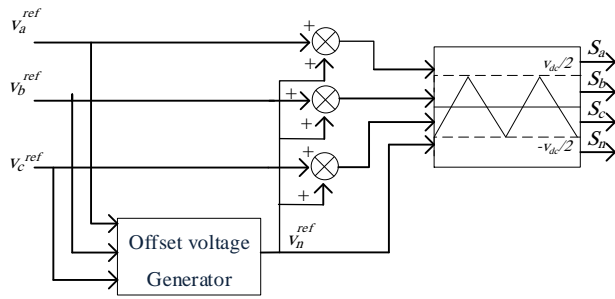


Fig. 2. Carrier based PWM technique [38]

III. PROPOSED CONTROL APPROACH

This section presents the design methodology of FB-GWC. As shown in Fig.3, the proposed voltage mode control strategy is subdivided into two main parts. The first part in which the differential flat model of the FL-VSI is used to derive the control inputs as function of the candidate flat outputs references and their successive derivatives. Meanwhile, the second part considers the use of a GW based controller to search for an optimum correction of the flat output references in such a way to ensure desired trajectory tracking performances especially in the presence of substantial nonlinear and/or unbalanced loads.

In the following, the flatness parameterization principle is first given, and then it is applied to check the flatness of the studied system. Afterward, the principle of the GW based tracking controller is provided. The end of this section summarizes the control flow implementation.

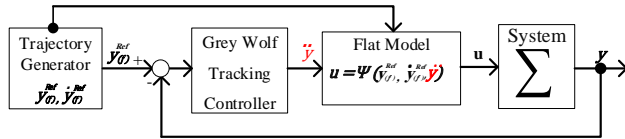


Fig. 3. Block diagram of the proposed FB-GWC.

A. Flatness description

The differential flatness is a structural property of some controllable dynamic systems. The property allows an explicit parameterization of both system inputs and states variables as function of a set of specified variables, commonly known as flat outputs, and a finite number of their time derivatives without requiring the resolution of differential equations [34].

In other words, the general system model $\dot{x} = f(x, u)$ is considered as a flat system if its corresponding states vector $x \in \mathbb{R}^n$ and input vector $u \in \mathbb{R}^m$ can be basically expressed as function of the candidate flat outputs given by:

$$y = \Lambda(x, u, \dot{u}, \dots, u^{(p)}), y \in \mathbb{R}^m \quad (6)$$

Accordingly, the system variables should take the form:

$$x = Y(\dot{y}, \ddot{y}, \dots, y^{(r)}) \quad (7)$$

$$u = \Psi(y, \dot{y}, \ddot{y}, \dots, y^{(r+1)}) \quad (8)$$

The flatness property is more useful if the candidate flat outputs correspond to the physical outputs of the system. In this way, the flatness-based description of most of power applications has gained in importance due to the existence of numerous output candidates having with straight mathematical and physical significance. The expressions given by (7) and (8) concord with the flatness property and allow a simple calculation of feed forward control law according to the desired flat outputs trajectories. The control law is finally designed to satisfy the control objectives and guarantee that the trajectories of the inputs and outputs are smooth.

B. Flatness of the studied system

Following the control objectives, the dq -axes energies presenting the electrostatic energy stored in the capacitor filters C_f and the homopolar output voltage v_{c0} are proposed to be the flat output candidates. Then, the flat output vector y can be expressed as follow:

$$y = \begin{bmatrix} y_0 \\ y_d \\ y_q \end{bmatrix} = \begin{bmatrix} v_{c0} \\ \frac{1}{2} C_f v_{cd}^2 \\ \frac{1}{2} C_f v_{cq}^2 \end{bmatrix} \quad (9)$$

According to part A, the parameterization (7) consists in the formulation of all system state variables as a function of the candidate flat output components and their respective derivatives. Regarding the proposed flat output vector (9), the $0dq$ -components of the output voltage can be expressed as follow:

$$v_c = [v_{c0}, v_{cd}, v_{cq}]^T = [y_0, \sqrt{\frac{2y_d}{C_f}}, \sqrt{\frac{2y_q}{C_f}}]^T \quad (10)$$

After a derivation of the flat output vector (9), the $0dq$ -components of the line current can be expressed as:

$$i = C_f \frac{d}{dt} v_c + \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & -\omega \\ 0 & \omega & 0 \end{bmatrix} v_c + i_L \quad (11)$$

where $\frac{d}{dt} v_c = [\dot{y}_0, \frac{\dot{y}_d}{\sqrt{2y_d C_f}}, \frac{\dot{y}_q}{\sqrt{2y_q C_f}}]^T$

Notice that i_L is an external disturbance.

The last step to rule on the flatness of the studied system is to verify that the input vector can be parameterized as a function of the candidate flat output components and their successive derivatives. Regarding the differential equation of the system given in (3) and the expressions of the state variables defined in (10) and (11), the input vector $u = [u_0, u_d, u_q]^T$ can be expressed as follow:

$$\mathbf{u} = \mathbf{v}_c + \begin{bmatrix} R_f + 3R_n & 0 & 0 \\ 0 & R_f & L_f\omega \\ 0 & -L_f\omega & R_f \end{bmatrix} \mathbf{i} + \begin{bmatrix} L_f + 3L_n & 0 & 0 \\ 0 & L_f & 0 \\ 0 & 0 & L_f \end{bmatrix} \frac{d}{dt} \mathbf{i} \quad (12)$$

where the expression of the theoretical line current derivation term is given in relation (12).

$$\frac{d}{dt} \mathbf{i} = \frac{d^2}{dt^2} \mathbf{v}_c + \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & -\frac{\omega}{C_f} \\ 0 & \frac{\omega}{C_f} & 0 \end{bmatrix} \frac{d}{dt} \mathbf{v}_c + \frac{d}{dt} \mathbf{i}_L \quad (13)$$

$$\text{with : } \frac{d^2}{dt^2} \mathbf{v}_c = \begin{bmatrix} \ddot{y}_0 \\ \frac{\ddot{y}_d}{\sqrt{2y_d C_f}} - \frac{\dot{y}_d^2}{2y_d \sqrt{2y_d C_f}} \\ \frac{\ddot{y}_q}{\sqrt{2y_q C_f}} - \frac{\dot{y}_q^2}{2y_q \sqrt{2y_q C_f}} \end{bmatrix}$$

Regarding Eq. 10, 11, 12 and 13, the input vector can be parameterized as a function of the candidate flat output components and their successive derivatives.

$$\mathbf{u} = \Psi(\mathbf{y}, \dot{\mathbf{y}}, \ddot{\mathbf{y}}) \quad (14)$$

To conclude, the studied system fulfills the flatness conditions expressed in (7) and (8) with \mathbf{y} as the flat output vector and \mathbf{u} as the input vector. The control goal is satisfied only if the parameters are exactly known and if there are no disturbances. Therefore, in the practice, the flat model should be extended with a tracking controller in order to follow the planned reference trajectories even in the presence of model error and load disturbance.

C. Grey Wolf Based Tracking Controller

In order to ensure that the measured flat outputs track appropriately the desired flat output trajectories, a GW based tracking controller is proposed. Grey wolf is a newly developed heuristic algorithm to handle nonlinear optimization problems[39-43]. It simulates the social hierarchy and hunting behavior of grey wolves in nature like searching, encircling and attacking the prey. It moves the wolves (agents) toward prey by updating location vector, which is an average of best locations of the pack. As reported in the literature, this algorithm presents advantages in terms of low computing complexity, solution accuracy, convergence independence of initial conditions and its ability to deal with local minima. In this subject, a comparative study with twenty nine tests [39] with Particle Swarm Optimization (PSO), Gravitational Search Algorithm (GSA), Differential Evolution (DE), Evolutionary Programming (EP), and Evolution strategy (ES) has shown that GW algorithm is highly competitive. These proprieties are particularly important from the point of view of real time implementation.

The construction mechanism of this algorithm involves three steps: objective formulation and evaluation, optimal hunting distance calculation, control terms selection.

Step 1) Objective function

The proposed optimization based tracking controller acts to find the minimal correction control terms ($\ddot{\mathbf{y}}$) which is then used by the differentially flat model (14) in such a way to ensure an adequate control performance under large load disturbances and model inaccuracy. For this aim, a cost function \mathbf{G} on the flat output error is proposed.

$$\mathbf{G} = \begin{pmatrix} k_0 & 0 & 0 \\ 0 & k_d & 0 \\ 0 & 0 & k_q \end{pmatrix} \boldsymbol{\varepsilon} \quad (15)$$

where $\mathbf{G} = [G_1, G_2, G_3]^T$ is the objective vector. Terms G_1 , G_2 and G_3 are respectively associated with the error in the Odq flat outputs, i.e $\boldsymbol{\varepsilon} = [(y_0^{\text{ref}} - y_0), (y_d^{\text{ref}} - y_d), (y_q^{\text{ref}} - y_q)]^T$. k_0 , k_d and k_q are constant gains whose the synthesis is presented in the following part.

Step 2) Optimal hunting distance calculation

The algorithm mechanism search three best solutions in regard of the global possible hunting solutions and calculate the candidate input control. Regarding the chosen cost function each dqo-input control axis is decoupled, the first three best intermediate input control variables can be formulated as follows

$$\begin{cases} \ddot{y}_{x_1} = (1 - M_{x_1})\ddot{y}_{x_\alpha} - G_x \\ \ddot{y}_{x_2} = (1 - M_{x_1})\ddot{y}_{x_\beta} - G_x \\ \ddot{y}_{x_3} = (1 - M_{x_1})\ddot{y}_{x_\delta} - G_x \end{cases} \quad x = \{d, q, o\} \quad (16)$$

Where $\ddot{y}_{x_1}, \ddot{y}_{x_2}, \ddot{y}_{x_3}$ are intermediate input control variables. $\ddot{y}_{x_\alpha}, \ddot{y}_{x_\beta}, \ddot{y}_{x_\delta}$ are the first three best optimal solutions to distance, $M_{x_1} = (2 a_x r_{x_k})^2 - 2 a_x^2 r_{x_k}$ $x \in \{d, q, o\}, k \in \{1, 2, 3\}$, r_{x_k} are random vectors which values are within $[0, 1]$, a_x are linearly decreased from 2 to 0 over. Finally, by the use of the predefined intermediate control variables, the candidate dq-input control axes are calculated by:

$$\ddot{y}_x[k+1] = \frac{\ddot{y}_{x_1} + \ddot{y}_{x_2} + \ddot{y}_{x_3}}{3} \quad x = \{d, q, o\} \quad (17)$$

Step 3) Control terms selection

As presented in the previous part, the final design step consists in the evaluation of the established control solutions in step 2. Herein, the established control solutions are parameterized by their specified distance $A_x = 2a_x r_{x_k} - a_x$. Therefore, the input control (d-axis) is selected as follow:

$$\begin{cases} \ddot{y}_x = \ddot{y}_x[k+1] & \text{if } (|A_x| < 1) \\ \ddot{y}_x = \ddot{y}_x[k] & \text{if } (|A_x| > 1) \end{cases} \quad (18)$$

The established candidate input control, in step 2, with its specified distance A_x is evaluated (A_x compared to 1) in order to predict the future input control

D. Reference Trajectory Generation

The reference trajectory used by the GW algorithm and the flat model is obtained through a second order low-pass filter. Note that this choice allows limiting the derivative terms and does not influence the required computation time. Therefore, desired trajectories of the flat outputs and its first derivative are defined by:

$$\begin{cases} y_{x(f)}^{Ref} = \left(1 - e^{-\frac{t-t_0}{\tau}} - \frac{t-t_0}{\tau} e^{-\frac{t-t_0}{\tau}}\right) (y_x^{Ref} - y_{x_0}) + y_{x_0} \\ \dot{y}_{x(f)}^{Ref} = \frac{dy_{x(f)}^{Ref}}{dt} \end{cases} \quad (19)$$

where indices (Ref, f, t₀) mean respectively reference, filtered and initial time.

In summary, the input control vector \mathbf{u} of the inverter are computed thanks to the planned flat output trajectories $(\mathbf{y}_{(f)}^{Ref}, \dot{\mathbf{y}}_{(f)}^{Ref})$, and the correction control term $(\dot{\mathbf{y}})$ generated by the GW tracking controller.

$$\mathbf{u} = \Psi(\mathbf{y}_{(f)}^{Ref}, \dot{\mathbf{y}}_{(f)}^{Ref}, \dot{\mathbf{y}}) \quad (20)$$

E. Implementation Details

The step-by-step operation of the proposed control strategy at each sample time (T_s) is listed below.

1. The measured current and voltage load at time k are transformed to their dq0-components.
2. The actual 0dq-component of the output voltage are used by the GW controller to predict the optimal value of the correction control term $\dot{\mathbf{y}}$. The algorithm process is depicted in Fig. 4 and described below:
 - The cost function is computed for each agent, which allows to select the three best solutions.
 - Each pack element updates its position following Eqs (18), (19) and (20).
 - The updated agents $(\dot{y}_{x_{\alpha\beta\delta}}(k+1))$ with $j=1, \dots, N$ are classified to obtain the best one.
 - The process is repeated until the iteration number k_{max} .

- In the last step, the best solution $\dot{y}_{x_{\alpha}}(k+1)$ is selected.
3. The resulted optimal solution of the GW algorithm present the correction control term $\dot{\mathbf{y}}$ of the flat model, which is used to compute the inverter input controls thanks to equation (8).
 4. For efficient working of the proposed strategy, the consumed time by the whole control (GW controller, park transformations and the flat model of the system) should be set sufficiently below the sampling periode (T_s). Then, the parameters of the on-line optimization algorithm are to be selected carefully. For this consideration, the research agents number (N) is limited to 4, while the maximum iteration number (k_{max}) setting is provided in the next section.

IV- SIMULATION RESULTS AND DISCUSSIONS

In this part, the GW tracking controller parameters selection is derived and the effectiveness of the control in terms of voltage harmonic contents and unbalance reduction is discussed. The main parameters taken into account for the simulation study are given in table I.

TABLE I

STUDIED SYSTEM PARAMETERS

DC bus voltage	400 V
AC bus	110 V RMS, 60 Hz
Switching frequency	20 kHz
Capacitor (C_f)	40 μ F
Inductor (L_f)	2 mH
Inductor (L_n)	0.2 mH

A. GW Parameters Selection

The GW tracking controller parameters selection considers three indicators:

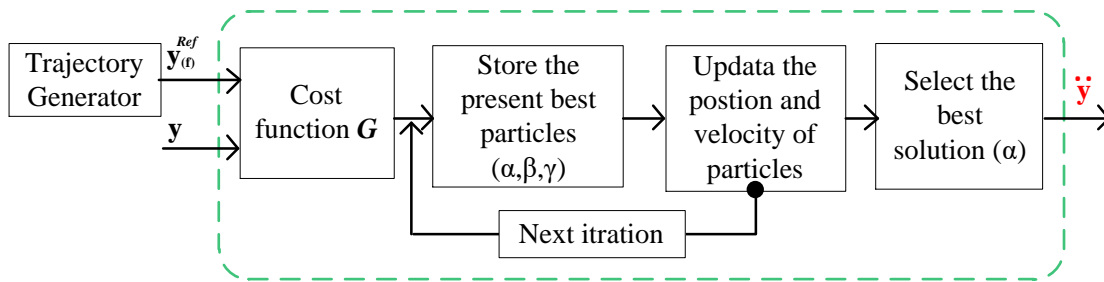


Fig. 4. Block diagram of the GW based tracking controller.

The total harmonic distortion (THD) of the output voltage
The voltage unbalance factor (VUF)

The needed calculation time of the GW algorithm.

For the above consideration, two load scenarios are considered. The THD criterion is evaluated under balanced per-phase nonlinear loads that consist of single-phase diode rectifiers with output RC circuits ($R = 40 \Omega$, $C = 150 \mu\text{F}$). Besides, the VUF, is based on an unbalanced linear resistive loading scenario where, in fact, the phase 'b' is opened while phases 'a' and 'c' are equally loaded ($R = 25 \Omega$). The following calculation can determine the percentage of VUF unbalance defined by [34].

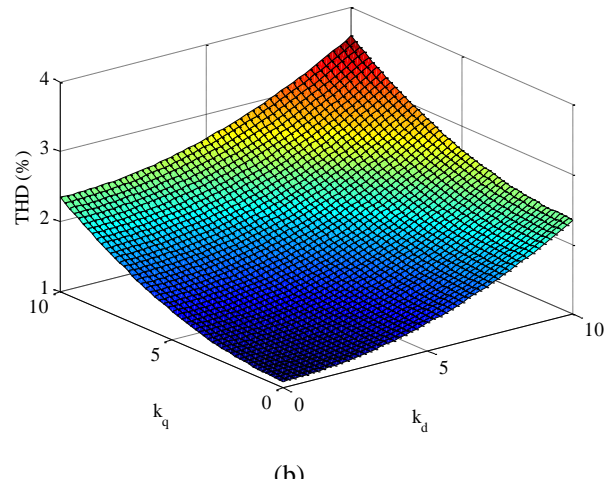
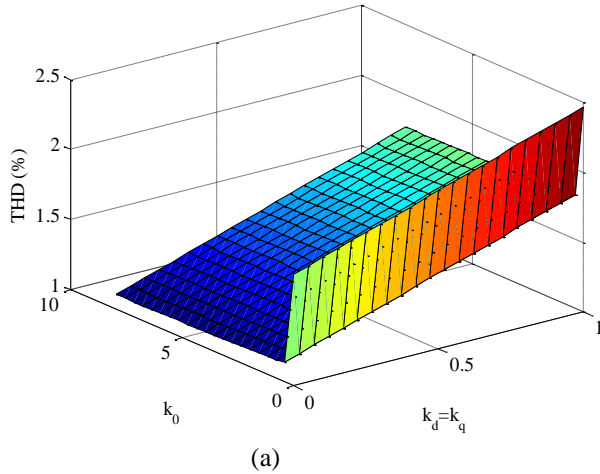


Fig. 5. Influence of the cost function odq-axis parameters on the voltage THD evolution: (a) k_0 gain influence when gains k_d and k_q are set equal, (b) k_d and k_q gains influence when k_0 is set to 10.

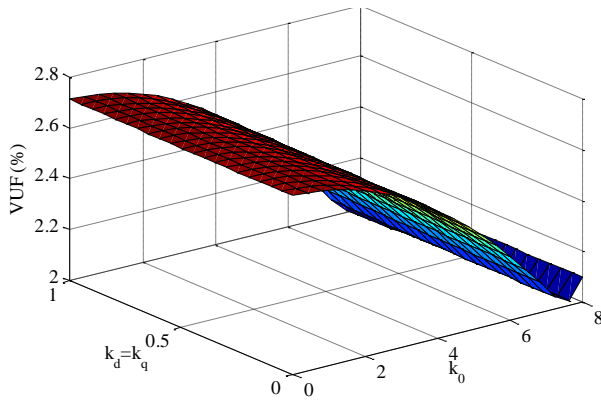


Fig. 6. VU-Factor versus changes in the odq-axis objective functions parameters.

$$\text{VUF (\%)} = \frac{\max(v_a, v_b, v_c) - \min(v_a, v_b, v_c)}{V_{avg}} \times 100 \quad (21)$$

For each load scenario, the cost function parameters are changed sequentially. In the left side of Fig. 6, the parameter k_0 is set to 7.5 and dq-axis gains (k_d and k_q) are changed from 0.1 to 10. Herein, the THD rate is reduced when using

dq-axis gains smaller than 5 and can be minimized for gains around 1. In the right side of Fig. 6, the gain k_0 is changed from 0.1 to 8 and dq-axis gains are taken equal and changed from 0.1 to 1. It can be seen that the THD evolution is more sensitive to the change dq-axis gains.

The evolution of VUF according to the cost function parameters is illustrated in Fig. 7. Thereafter, k_d and k_q are taken equal and are linearly changed from 0 to 1 while k_0 is changed from 0.1 to 8. The VUF rate, as it can be appreciated, is further reduced for greater gains of k_0 . This rate is minimized for k_0 to 7.5 which satisfy $\text{VUF} < 2.2 \%$.

implementation. The chosen value of N for our practical implementation is equal to 6.

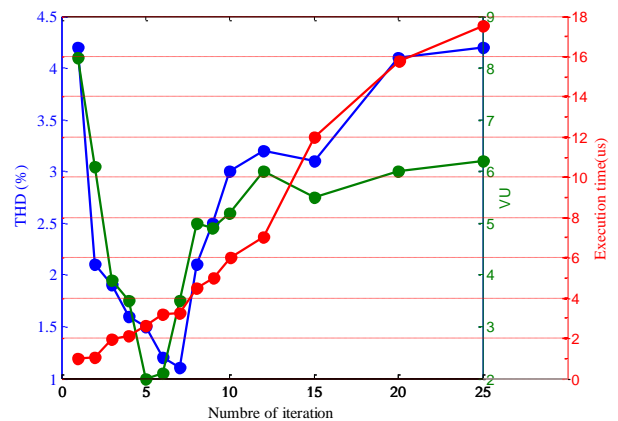


Fig. 7. Iteration number influence on the: THD (blue color), VUF (green color) and execution time (red color).

In summary, dq-axis gains (k_d and k_q) values are related to the voltage quality and properly selected gain allows reducing the THD rate. Gain k_0 acts on the voltage symmetry and allows reducing VUF rate. Notice that the iteration number is set to 6 in this test.

To illustrate the influence of the GW calculation burden on the effectiveness of the proposed tracking controller, the iteration number (N) is changed sequentially and several simulation tests under loading scenario are done. Fig. 7 groups the results obtained from the corresponding THD rate, the VUF rate and the calculation time. As it can be appreciated, the iteration number is a key parameter to satisfy the control effectiveness. Indeed, the obtained results show that iteration number 6 allows obtaining satisfactory control performance: i.e reduced THD and VUF rates with a minimized time calculation regarding real-time

A. Performance illustration

To underline the performance of the proposed controller, comparison tests with open-loop control based model and a conventional PI control method [43] are proposed. The open loop control is based on the flatness model where \ddot{y} is set to zero in Fig. 3. The scheme principle of the conventional PI control is shown in Fig. 8.

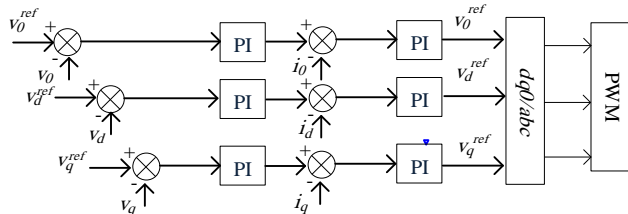


Fig. 8. Block diagram of a conventional PI based FL-VSI control[45].

The PI parameters are selected by the use of pole placement method where the outer loop parameters $k_{pv}=5.33 \times 10^{-3}$ $k_{iv}=1.42$ and the inner loop $k_{pi}=120$ et $k_{ii}=316 \times 10^3$.

The compared three control strategies are firstly tested under linear load based on balanced resistive loads where $R_a = R_b = R_c = 15 \Omega$. The obtained RMS voltages are presented in Fig. 9). As it can be appreciated, the open-loop control can be sufficient under ideal working conditions (balanced linear load where all parameters are known). Notice that the THD are equal to 2.1 for the open-loop, PI and proposed FBC-GWC controllers respectively. The compared controllers robustness is evaluated under unbalanced nonlinear loading conditions. The loads consist of single-phase diode rectifiers that cause zero sequence distortion at the odd triplen harmonics. For instance, load current phase a cause 2% of 3rd harmonic and 5% of 9th harmonic. The open-loop model is no longer sufficient and the use of closed-loop controller is necessary to handle the unbalance and harmonic issues. In

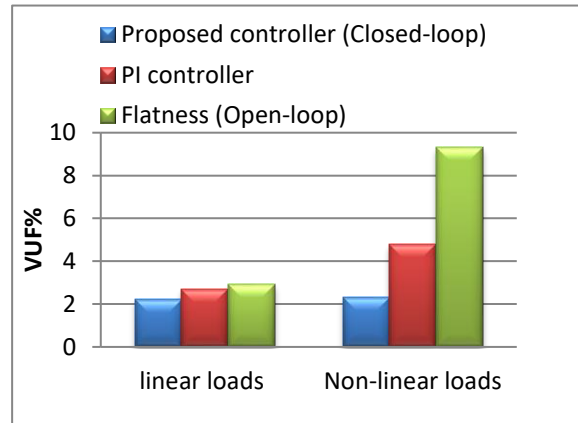
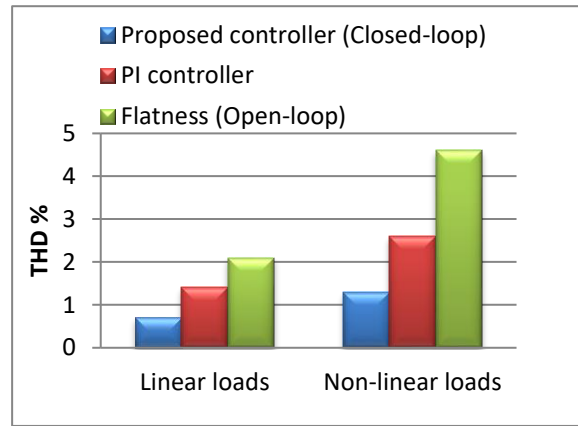
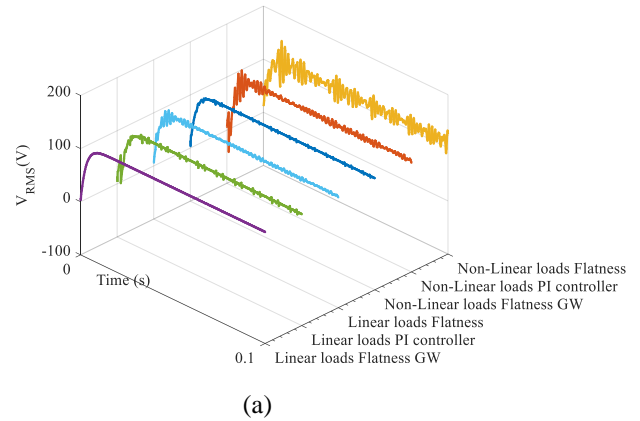


Fig.9. Obtained output voltage RMS and THD rate with open-loop, PI controller and closed-loop under balanced linear load and unbalanced nonlinear loads: (a) output voltage RMS (b) output voltage THD (c) output voltage VUF

this way the use of a conventional PI controller and the proposed controller allow to significantly improve the power quality of the output voltage. The computed VUF values that correspond to open-loop, PI and proposed controller are 9.3%, 4.8% and 2.3% respectively, the correspondents VUF are reported in Fig 9c. The voltage THD of phase “a” are 4.6%, 2.6% and 1.3%, respectively; the correspondents THD rates are reported in Fig 9b.

The proposed control scheme achieves the lowest VUF and voltage THD values, which corresponds to better power quality.

To evaluate the effect of input voltage variation on the voltage regulation, the DC bus is changed from 400 to 600 V at $t= 0.05s$ where the output RMS voltage reference is kept constant. As it can be appreciated, the AC voltage is not influenced by the DC bus voltage. The only condition is related to the controllability limit where the DC voltage level must be sufficient. The limit of DC bus voltage can be estimated as follow [46]: $V_{dc} \geq V_{RMS} 2\sqrt{2}/(\sqrt{3} m)$. For a modulation index $m = 0.5$, the minimal DC bus voltage to ensure controllability is around 360 V. Therefore, a DC bus voltage of 400 V is considered in this work.

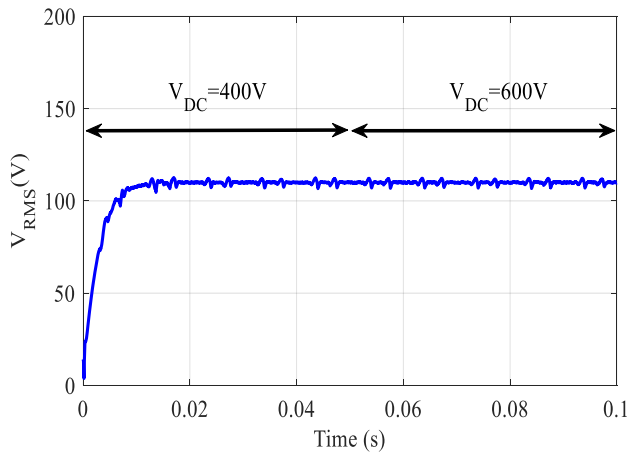


Fig.10. Obtained output voltage RMS under input voltage variation

IV. EXPERIMENTAL VERIFICATION

In order to validate the proposed control strategy, experiments are carried out. The control algorithm is implemented in a dSPACE (Micro-Auto-Box rapid

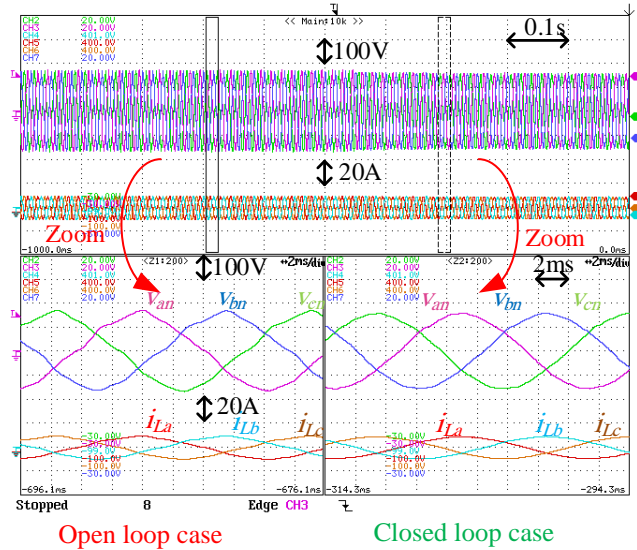


Fig. 11. Experimental results: load voltages and currents during steady state with balanced resistive load before and after activating the proposed control (prototyping platform). Different tests under different loading scenarios are performed to highlight the performance of the FB-GWC.

Fig.11. shows the steady state response of the load voltages and currents and their respective zooms under balanced resistive load before (open loop) and after activating the proposed control approach. The balanced resistive loads are set to $R_a= R_b= R_c=15 \Omega$. When the proposed control is enabled the load voltage waveforms become purely sinusoidal and the voltage THD decreased from 8% (open loop) to 0.8%.

Fig.12. shows the steady state response of the load voltages and currents and their respective zooms under unbalanced resistive load before (open loop) and after activating the proposed control approach. The considered unbalanced

resistive loads are set as $R_a=\infty \Omega$ and $R_b= R_c=15 \Omega$. It can be noticed that the load voltage are balanced when the proposed control is applied and the measured voltage THD is decreased from 8.5% (open loop) to 1.1%.

In case of single phase load ($R_a= R_b=\infty \Omega$ and $R_c=15 \Omega$), Fig. 13 illustrates the load voltages and currents responses and their respective zooms before (open loop) and after activating the proposed control approach. The load voltages are balanced. The noted voltage THD with the proposed control is about 1 %.

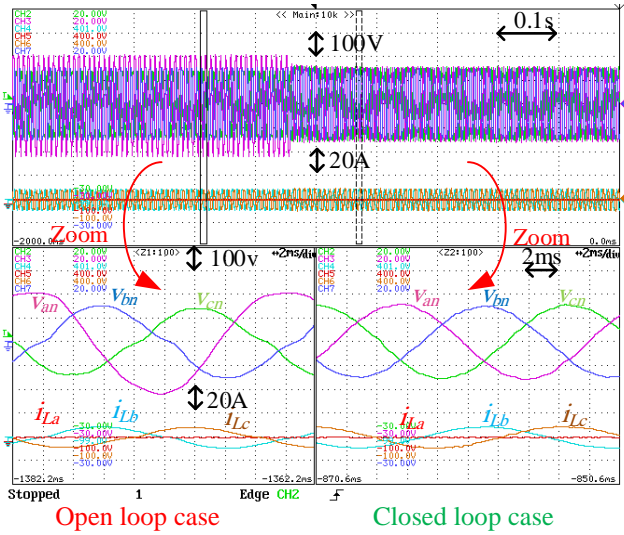


Fig. 12. Experimental results: load voltages and currents during steady state with unbalanced resistive loads ($R_a = \infty \Omega$ and $R_b = R_c = 15 \Omega$) before and after activating the proposed control.

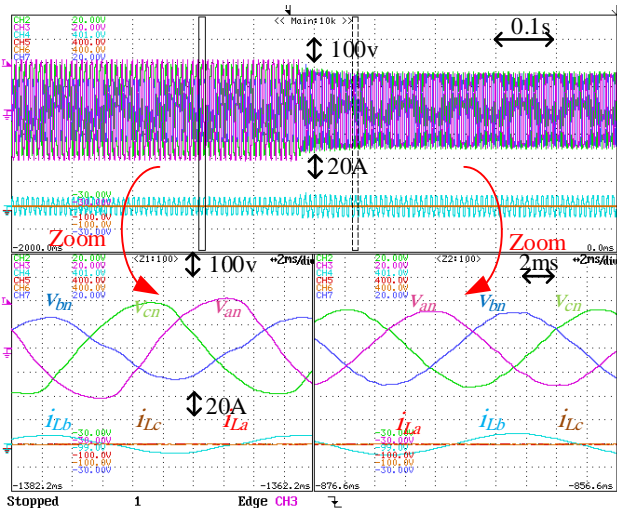


Fig. 13. Experimental results: load voltages and currents during steady state with unbalanced resistive loads ($R_a = R_b = \infty \Omega$ and $R_c = 15 \Omega$) before and after activating the proposed control.

Fig.14. shows the output voltages and currents during a 0%-to-100% step of a balanced resistive load. The transient response lasted for only 3 ms. As clearly illustrated, the control showed good stability with no oscillatory behavior. Fig.15. shows the output voltages and currents during a single phase load and after balanced three-phase load. The transient response lasted for only 1 ms. Moreover, the pure sinusoidal voltage and current waves represent less voltage and current harmonics

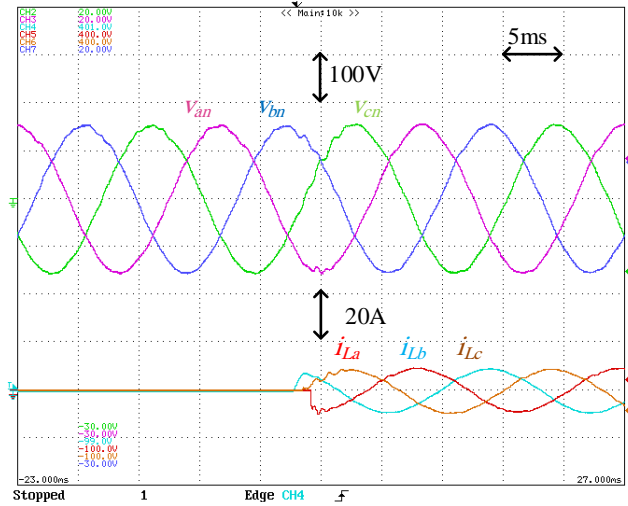


Fig.14. Transient of 0%–100% balanced resistive load.

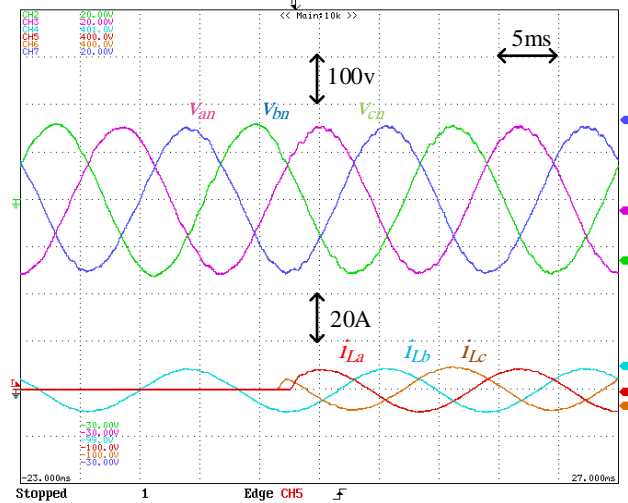


Fig. 15. Transient of single phase load to balanced resistive load.

To investigate the proposed approach performance in critical working conditions, a scenario of nonlinear unbalanced loads is considered. The considered unbalanced nonlinear loads are shown in Fig .16. The loads parameters are: phase a ($R_a = 80 \Omega$, $C_a = 1100 \mu F$), phase b ($R_b = 20 \Omega$, $C_b = 500 \mu F$) and phase c ($R_c = 100 \Omega$, $L_c = 30 mH$).

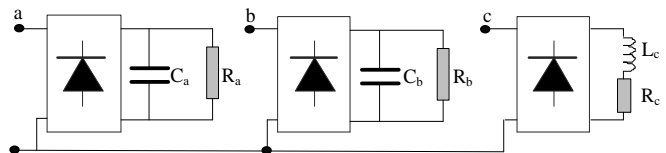


Fig. 16. Topology of the tested unbalanced nonlinear loads.

For this loading scenario, the output load voltages, and load currents waveforms before (open loop) and after activating the proposed control are shown in Fig.17. The voltage THD values are reported in Table 2. This test shows that the proposed control scheme allow keeping the load voltages balanced with a reasonable voltage THD.

Referring to these results, it can be concluded that in steady state with balanced or unbalanced linear or nonlinear loading conditions, the use of the proposed control approach allows to keep the load voltages balanced with a voltage THD less than 2%.

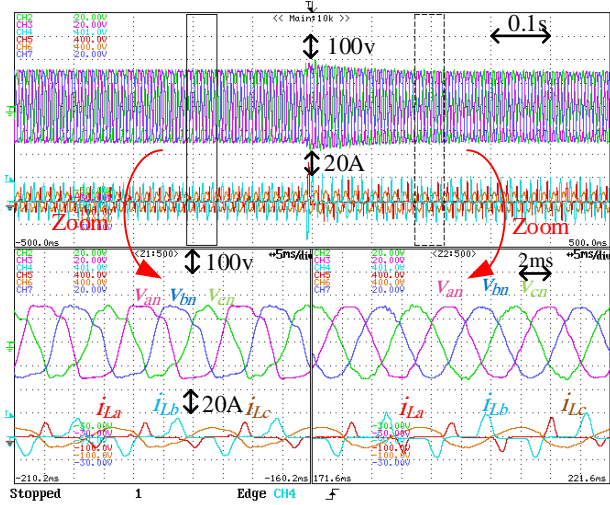


Fig.17. Experimental results: load voltages and currents during steady state with unbalanced nonlinear loads before and after activating the proposed control.

TABLE 2

LOAD VOLTAGE THD FOR A THREE-PHASE

THD (%)	Phase	Phase	Phase
	a	b	c
Open Loop	8.8	10.8	10.1
Flatness	1.8	1.7	2
Based GW Control			

The analysis of the reported THD values regarding robustness against parameter variations proves that the proposed control allows obtaining a lower voltage THD value.

The evolution of the (THD) versus the variations in L_f with balanced ($R_a=R_b=R_c=15\ \Omega$) and unbalanced ($R_a=\infty$ and $R_b=R_c=15\ \Omega$) loading conditions. the load voltage THD values of the proposed approach for different L values (1 mH, 2 mH, 3 mH) are shown in In Fig 18.

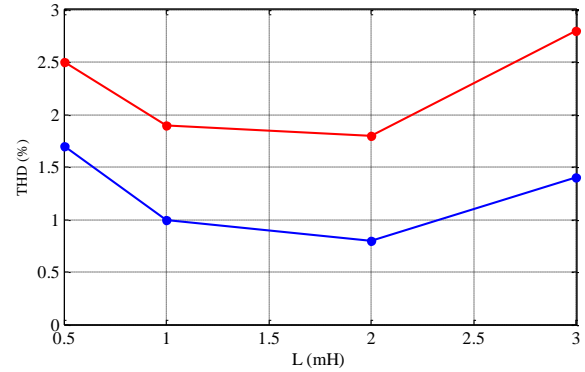


Fig.18. Load voltage THD versus value of Inductor (L_f) under balanced linear load (blue color) and unbalanced nonlinear loads (red color).

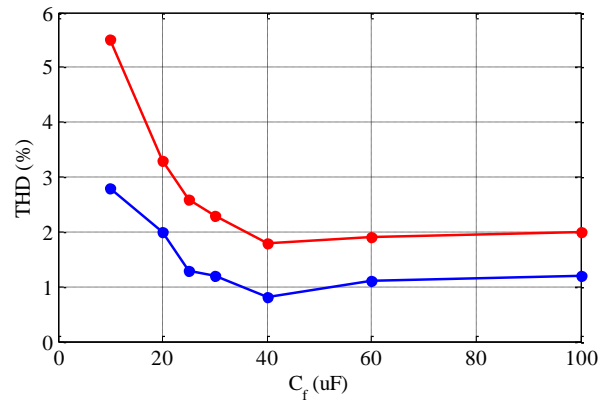


Fig.19. Load voltage THD versus value of capacitor (C_f) under balanced linear load (blue color) and unbalanced nonlinear loads (red color).

In Fig 19, the evolution of the load voltage THD versus the variations in C_f with balanced ($R_a=R_b=R_c=15\ \Omega$, blue curve) and unbalanced ($R_a=\infty$ and $R_b=R_c=15\ \Omega$, red curve) loading conditions, when the parameter L is set to 1 and C_f is changed from 20 μF to 100 μF . It can be seen that the THD evolution is less sensitive to the C_f superior 40 μF . The analysis of the fig 18 and 19 shows that the variations in C_f have more impact on the load voltages than the L_f variations.

V. Conclusion

In this paper a voltage control strategy based on a Flatness theory with a Grey Wolf tracking control (FB-GWC) has been proposed for a four-leg inverter with an output LC filter for use in Three-Phase Four-Leg Voltage Source Inverters. The proposed FB-GWC control scheme can be split into two subsystems: the flat model of the system and the GW tracking controller. The differentially flat model of the FL-VSI allows calculating the control inputs as differential functions of the flat outputs and their derivatives. The GW tracking controller provide the correction control effort used by the differentially flat model in such a way to ensure the desired trajectory

tracking tasks especially in case of the presence of heavy nonlinearity or unbalanced loads.

The provided experimental results demonstrated that the proposed control approach allows keeping the load voltage balanced with a reasonable voltage THD rate when the system is subject to disturbances like nonlinear and unbalanced loads.

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